

EAST - [10.718562.wsp:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

L1: (35123) memory adj2 chip

L2: (37667) voltage adj2 select\$4

L3: (5339) sram with eeprom

L4: (87) 1 same 2

L5: (1747) 1 and 2

L6: (87) 3 and 5

L7: (87) 4 not 6

L21: (4437) 365/226

L22: (3248) 365/189.09

L23: (220085) 365/

L24: (200) 5 and 21

L25: (183) 5 and 22

L26: (1226) 5 and 23

L27: (319) 26 and (first with second with voltage with (lower or higher))

L28: (234) 27 not (24 or 25)

L29: (34) (HONMA near2 KAZUKI).in.

L30: (170) (KAWAJIRI near2 YOSHIKI).in.

L32: (35) (SUGAWARA near2 MIKIO).in.

L34: (4) (SONOYAMA near2 HIROFUMI).in.

L35: (167) (WADA near2 MASASHI).in.

Failed

Saved

Search

DBs

USPAT;US-PGPUB;EPO;JPO;DERWENT;IBM;TDS

Default operator

OR

Europe

Highlight all hit terms initial

BRG form

ISAR form

Image

Text

HTML

	U	1	2	Document ID	Issue Date	Inventor	Current OR	Current XRe Retrieval	C	Title	Page	S	C	P
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5243577 A	19930907	Ueda, Toshiyuki et al.	365/229	307/64; 365/228		Electronic apparatus	8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5384748 A	19950124	Sanemitsu, Yoshikado	365/229	365/154; 365/156		Memory card with two SRAM	8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6434044 B1	20020813	Gongwer, Geoffrey Steven et al.	365/185.18	365/185.11; 365/226		arrows with different data hol Method and system for generation and distribution o	12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

NUM

2:58 PM